



**Admission to the 1<sup>st</sup> year of a Master's Degree with unrestricted access – A.A. 2020-21**

*Electronic Engineering (Class LM-29)*

**Admission requirements for the course of study**

Candidates who meet the following requirements can enroll in the master's program (as referred to in section 1 of the Call):

- a) possessing a bachelor's degree in the class L-8 Information Engineering or in the class L-9 Industrial Engineering or equivalent qualification pursuant to the Interministerial Decree 09/07/2009. Alternatively, students must have completed a university degree lasting at least three years recognized as suitable by the Board of Directors of the Course of Study
- b) a minimum curricular requirement set out in terms of ECTS (or any equivalent experience) acquired during any university course for the following groups of scientific areas (SSD)

<b>Groups of scientific areas (SSD)</b>	<b>min ECTS</b>
INF/01, ING-INF/05, MAT/02, MAT/03, MAT/05, MAT/06, MAT/07, MAT/08, MAT/09, SECS-S/02, CHIM/03, CHIM/07, FIS/01, FIS/03, FIS/07	36
ING-IND/31, ING-INF/01, ING-INF/04	18

**Procedure for the verification of the admission requirements**

All applicants must complete the application form (as referred to in section 2 of the Call) and comply with its terms and conditions. The Examination Board will check up on the suitability of all candidates in terms of personal preparation through the analysis of their curricula and, according to the University Rules, on any possible obsolescence or gaps in terms of knowledge if the credits have been earned for more than 6 years.

The Examination Board may find it necessary to ascertain the adequacy of candidates' knowledge in subjects specific of the Course of Study, as well as the personal preparation, by means of an interview. The interview will be necessary also if the candidates do not possess an English certification with minimum level B2 in the CEF (Common European Framework).

The list of the candidates both admitted and to be interviewed will be **posted by September 18<sup>th</sup>, 2020**. The interview will take place on September **23<sup>rd</sup> 2020** at **10:00 a.m.** at the Department of Electric, Electronic and Computer Engineering, DIEEI, **room D02** Building 14, Viale A. Doria 6, 95125 Catania. If necessary, for justified reasons, the candidate may request to carry out his interview via videoconference, by sending an email to the President of the Course of Study, prof. Salvatore Pennisi (salvatore.pennisi@unict.it), within September 21<sup>st</sup>, 2020.

The candidates will be evaluated by the Examination Board as being either ADMITTED or NOT ADMITTED.

Candidates may enroll in the Course of Study (as referred to in section 4 of the all) from September **25<sup>th</sup> 2020 to October 12 2020**.



### **Examination Board**

The Examination Board (as referred to in section 3.1. of the Call) is made up of:

- Prof. Salvatore Pennisi
- Prof. Egidio Ragonese
- Prof. Alfio Dario Grasso
- Prof. Gianluca Giustolisi (Substitute)
- Prof. Santi Agatino Rizzo (Substitute)

### **List of admitted students**

Once all the required formalities have been completed, the list of students admitted also as a result of the interview will be drawn up and published.

### **Responsible for the procedure**

The person in charge of all the administrative procedures related to the announcement is Mr. Francesco D'Asero from the Career Services Office of the technical and scientific sector. She has been identified in accordance with art. 5, paragraph 1, of the Italian legislation 241/90. She is available at University Campus Office in via Santa Sofia nr. 64, Catania - +39 095/7382051 – [fdasero@unict.it](mailto:fdasero@unict.it)