



Ammissione al I anno dei corso di laurea magistrale a numero non programmato – A.A. 2017-18
Electrical Engineering (Classe LM-28)

Requisiti di ammissione al corso di studio

Possono iscriversi al corso di laurea magistrale in Electrical Engineering (LM-28) i candidati in possesso dei seguenti requisiti di partecipazione (punto 1 dell'Avviso):

a) possesso di uno dei seguenti titoli:

- Laurea ai sensi del DM270/04 conseguita nella classe delle lauree di ingegneria industriale (Classe L-9 del DM 16 marzo 2007)
- Laurea ai sensi del DM270/04 conseguita nella classe delle lauree di ingegneria dell'informazione (Classe L-8 del DM 16 marzo 2007)
- altro titolo di studio (eventualmente conseguito all'estero) riconosciuto idoneo dai competenti organi, previa verifica di adeguati requisiti curriculari minimi:
 - 6 CFU in MAT/02 o MAT/03
 - 12 CFU in MAT/05
 - 6 CFU in CHIM/07 o ING-IND/22
 - 6 CFU in ING-IND/31
 - 6 CFU in ING-INF/04.

Per gli studenti stranieri, ovvero in possesso di laurea con percorso curriculare non definibile in termini di CFU, il valore di 6 o 9 CFU è da intendersi come un esame sostenuto nel corrispondente settore scientifico-disciplinare o settore equipollente. Il valore di 12 CFU è da intendersi come due esami sostenuti nel corrispondente settore scientifico-disciplinare o settore equipollente.

b) possesso dei requisiti curriculari di seguito indicati:

Gruppi di Settori Scientifico-Disciplinari (SSD)	min CFU
INF/01, ING-INF/05, MAT/02, MAT/03, MAT/05, MAT/06, MAT/07, MAT/08, MAT/09, SECS-S/02, CHIM/03, CHIM/07, ING-IND/22, FIS/01, FIS/03, FIS/07	36
ING-IND/31, ING-IND/32, ING-IND/33, ING-INF/04	12

Modalità di verifica dell'adeguatezza della preparazione

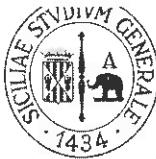
Tutti i candidati devono compilare la domanda di partecipazione (punto 2 dell'Avviso) procedendo nei termini e secondo le modalità previste.

La commissione verifica l'adeguatezza della preparazione personale dei candidati attraverso l'analisi dei curricula, fermo restando quanto previsto dal Regolamento didattico d'Ateneo sulla valutazione della non obsolescenza dei contenuti conoscitivi dei crediti conseguiti da più di 6 anni.

I candidati ammessi potranno **iscriversi** (punto 4 dell'Avviso) al corso di studio a partire dalla pubblicazione dell'elenco degli ammessi entro il 13 ottobre 2017 (dal 14 al 31 ottobre 2017 iscrizione tardiva con mora).

Entro il 19 settembre 2017 sarà pubblicato l'elenco dei candidati per i quali la Commissione, sulla base dell'analisi del curriculum personale, ritiene inoltre necessario verificare l'adeguatezza della preparazione personale attraverso un colloquio individuale, che si svolgerà giorno **29 Settembre alle ore 9:00** presso l'**aula P7 del Dipartimento di Ingegneria Elettrica Elettronica e Informatica, Viale A. Doria 6, edificio 3, Catania**. Il colloquio avrà come oggetto argomenti di base riguardanti il settore scientifico-disciplinare "ING-IND/31 - Elettrotecnica". Nel corso del colloquio,

AV



per i candidati non in possesso di certificazione, sarà verificata anche la conoscenza della lingua inglese *che dovrà essere non inferiore al livello A2 della classificazione del CEF (Common European Framework)*

A seguito del colloquio, il candidato può essere valutato dalla commissione come AMMESSO o NON AMMESSO.

Commissione esaminatrice

La commissione esaminatrice, di cui al punto 3.2 dell'Avviso, è composta da:

- Prof. Mario Cacciato
- Prof. Giovanni Muscato
- Prof. Nunzio Salerno
- Prof. Giuseppe Scarcella (supplente)

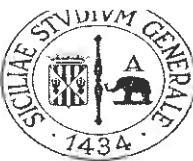
Elenco degli ammessi

Espletati tutti gli adempimenti previsti verrà stilato e pubblicato l'elenco degli ammessi a seguito di colloquio.

Responsabile del procedimento

Il Responsabile del procedimento amministrativo relativamente a tutte le procedure inerenti l'Avviso è individuato, ai sensi dell'art. 5, comma 1, della L. 241/90 nel dott. Francesco D'Asero - Ufficio Carriere studenti settore tecnico scientifico – Cittadella Universitaria Via S.Sofia 64 , Catania tel. 095/ 7382051 – fdasero@unict.it.

AP



Admission to the 1st year of a Master's Degree in Science with unrestricted access – A.A. 2017-18

Electrical Engineering (Class LM-28)

Admission requirements for the course of study

Candidates who meet the following academic requirements can enroll in the Master of Science in Electrical Engineering (LM-28), referred to in section 1) of the announcement, if they possess:

a) one of the following qualification requirements:

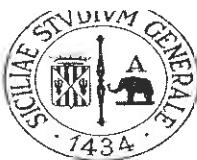
- a completed University Degree in Industrial Engineering in accordance with the Italian M.D. 270/04 (Class L - 9 of the M.D. - March 16, 2007)
- a completed University Degree in Computer Engineering, in accordance with the Italian M.D. 270/04 (Class L - 8 of the DM 16 March 2007)
- Another completed qualification (possibly from abroad) which may be recognized as equivalent by the competent bodies, after ensuring the adequacy of it through a minimum of qualifications requirements such as :
 - 6 credits in MAT/02 or MAT/03
 - 12 credits in MAT/05
 - 6 credits in CHIM/07 and ING-IND/22
 - 6 credits in ING-IND/31
 - 6 credits in ING-INF/04.

Overseas students who possess a University Degree with an undefined curricular path in terms of ECTS, any equivalent scores to 6 or 9 are to be regarded as exams which have been taken in an equally scientific capacity or equivalent industry sector. The value of 12 credits is to be regarded as two exams taken in a related scientific subject or equivalent industry sector.

a) the curricular requirements here below:

Groups of scientific areas (SSD)	min ECTS
INF/01 (INFORMATICS), ING-INF/05 (INFORMATION PROCESSING SYSTEMS), MAT/02 (ALGEBRA), MAT/03 (GEOMETRY), MAT/05 (MATHEMATICAL ANALYSIS), MAT/06 (PROBABILITY AND STATISTICS), MAT/07 (MATHEMATICAL PHYSICS), MAT/08 (NUMERICAL ANALYSIS), MAT/09 (OPERATIONS RESEARCH), SECS-S/02) STATISTICS FOR EXPERIMENTAL AND TECHNOLOGICAL RESEARCH), CHIM/03 (GENERAL AND INORGANIC CHEMISTRY), CHIM/07 (PRINCIPLES OF CHEMISTRY FOR APPLIED TECHNOLOGIES), ING-IND/22 (MATERIALS SCIENCE AND TECHNOLOGY) FIS/01 (EXPERIMENTAL PHYSICS), FIS/03 (PHYSICS OF MATTER), FIS/07 (APPLIED PHYSICS)	36
ING-IND/31 (ELECTRICAL ENGINEERING), ING-IND/32 (POWER ELECTRONIC CONVERTERS, ELECTRICAL MACHINES AND DRIVES), ING-IND/33 (ELECTRICAL POWER SYSTEMS) ING-INF/04 (SYSTEMS AND CONTROL ENGINEERING)	18

AM



The arrangements for evaluating the adequacy of preparation

All applicants must complete the application form (referred to in section 2 of the announcement) and comply with its terms and conditions.

The Examination Board will check up on the suitability of all candidates in terms of personal preparation through the analysis of their curricula and, according to the University Rules, on any possible obsolescence or gaps in terms of knowledge if the credits have been earned for more than 6 years.

Candidates may enroll (referred to in section 4 of the announcement) in the course of study from the date of publication of successful applicants, by the 13th of October, 2017. For late enrolment, from the 14th to the 31st of October, they will incur an additional fee.

The list of successful candidates, depending upon the assessment of the Examination Board, will be posted by September 19, 2017. Based on the analysis of the curricula received, the Examination Board may as well find it necessary to ensure the adequacy of candidates' knowledge and personal preparation by means of a final interview, which will take place on the **29th of September, 2017 at 9:00 a.m. at the Department of Electric Electronic and Computer Engineering (Room P7 of Building 3) in Via A. Doria 6, 95125 Catania**. A particular attention shall be paid during this interview to the basic principles and arguments covered by the whole scientific and disciplinary area of Electrical Engineering (ING-IND/31). Furthermore, as to those who do not possess a formal certificate, their command of English language will be tested too, which is not to be below the A2 level, in line with the CEF (Common European Framework).

Non-EU citizens who reside abroad and hold a student visa issued by a relevant, diplomatic office must submit the documentation (along with a declaration of value concerning their bachelor's degree or a formal certificate issued by their own academic authorities certifying the study path taken and all the exams passed) to the Foreign Students Office in Piazza Bellini, 19 (on the second floor) by September 26, 2016. There will also be an individual interview for them to go to, which is regarded as compulsory and selective for the final list of admissions to be published within a number of available seats. This will be due on October 4, 2016 at 10.00 a.m at the Department of Electric, Electronic and Computer Engineering (Room P7 of Building 3) in Via A. Doria 6, 95125 Catania. A particular attention shall be paid during this interview to the basic principles and arguments of the whole scientific and disciplinary area Electrical Engineering (ING-IND/31) covered by their respective bachelor's degrees. In addition to that, the command of the English language will be tested too, which is not to be below the A2 level, in line with the CEF (Common European Framework).

Following the interview, the candidates shall be eventually evaluated by the Examination Board as being admitted or not admitted.

Examination Board

The Examination Board, referred to in section 3.2 of the Announcement, is made up of:

- Prof. Mario Cacciato
- Prof. Giovanni Muscato
- Prof. Nunzio Salerno
- Prof. Giuseppe Scarcella (supplente)

List of admitted students

As a result of the interview and most importantly when all the required procedures are completed a list of admitted students will be drawn up and published.

Responsible for the procedure

The person in charge of all the administrative procedures related to the announcement is Francesco D'Asero from the Career Services Office of the technical and scientific sector. He has been appointed in accordance with art. 5, paragraph 1, of the Italian legislation 241/90. He is available at the University Campus Office in Via Santa Sofia 64, Catania. Tel: +39 095. 738 2051, E-mail: fdasero@unict.it